	Application No.	Applicant(s)
Notice of Allowability	10/789,738	DELORME ET AL.
	Examiner	Art Unit
	LAM T. MAI	2819
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to <u>2/27/04</u> .		
2. The allowed claim(s) is/are <u>1-20</u> .		
3. The drawings filed on 27 February 2004 are accepted by the Examiner.		
<ul> <li>4. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).</li> <li>a) All b) Some* c) None of the: <ol> <li>Certified copies of the priority documents have been received.</li> <li>Certified copies of the priority documents have been received in Application No.</li> <li>Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).</li> </ol> </li> <li>* Certified copies not received:</li> </ul>		
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	of this communication to file a reply IENT of this application.	complying with the requirements
5. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.		
6. CORRECTED DRAWINGS ( as "replacement sheets") mus	st be submitted.	
(a) 🗌 including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached		
1)  hereto or 2)  to Paper No./Mail Date		
(b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date		
Identifying indicia such as the application number (see 37 CFR 1, each sheet. Replacement sheet(s) should be labeled as such in t	.84(c)) should be written on the drawin he header according to 37 CFR 1.121(c	ngs in the front (not the back) of d).
7. DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT	sit of BIOLOGICAL MATERIAL n FOR THE DEPOSIT OF BIOLOGICA	nust be submitted. Note the AL MATERIAL.
Attachment(s) 1. ☑ Notice of References Cited (PTO-892)	5.	atent Application (PTO-152)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	6. Interview Summary	(PTO-413),
<ul> <li>Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 2/04;5/05</li> <li>Examiner's Comment Regarding Requirement for Deposit</li> </ul>		e nent/Comment ent of Reasons for Allowance
of Biological Material	9. ☐ Other	2. Franco to Franco

## **DETAILED ACTION**

## Information Disclosure Statement

The information disclosure statement (IDS) submitted on 2/27/04 and 5/19/05 have been considered by the examiner.

# Specification

The lengthy specification has not been checked to the extent necessary to determine the presence of all possible minor errors. Applicant's cooperation is requested in correcting any errors of which applicant may become aware in the specification.

# Allowable Subject Matter

Claims 1-20 are allowable.

The following is an examiner's statement of reasons for allowance: Claims 1-20 are allowed over the prior art of record. The prior art of record, considered individually or in combination, fails to fairly teach or suggest the claimed input bias current cancellation circuit comprising, among other limitations, a novel and unobvious limitation of "a second current source which provides a second bias current to a bipolar tracking transistor and arranged a base current copy circuit providing a base current to the tracking transistor which required to achieve a substantially equal collector current in the tracking transistor and the copy circuit further arranged to provide first and second bias current cancellation currents lcncl1, lcncl2 to base of first and second input transistors respectively, lcncl1 lcncl2 ltrk thereby reducing the input stage input bias currents"

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structurally and functionally interconnected with other limitations in the manner as cited in claims 1-20.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

#### **Cited References**

The prior art made of record and not replied upon considered pertinent to application's disclosures. The cited references relate to differential amplifier circuit with input bias cancellation circuit.

### Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to LAM T. MAI whose telephone number is (571)272-1807. The examiner can normally be reached on 6:00 am - 4:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mike Tokar can be reached on (571) 272-1812. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Lam T. Mai Art Unit 2819